

FW-079

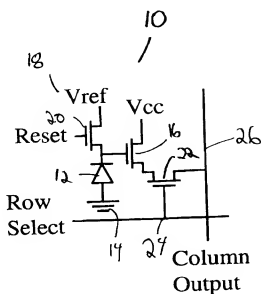


FIG. 1A

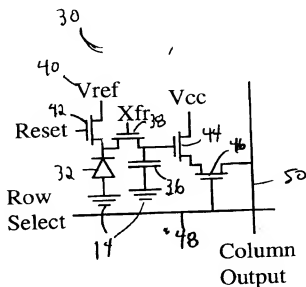


FIG. 1B

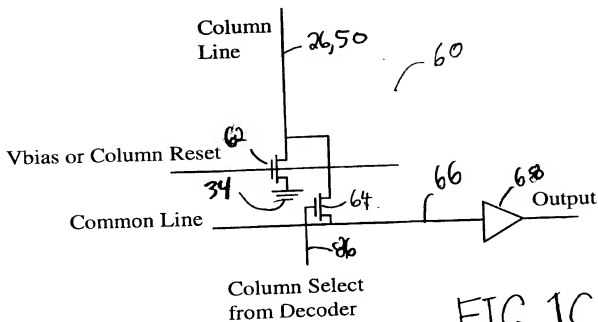


FIG. 1C

10001438-102401

FX-079

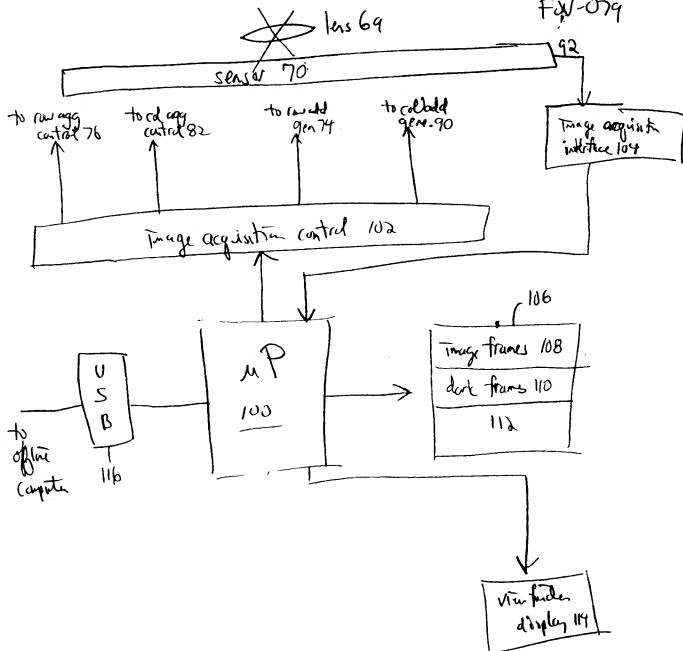
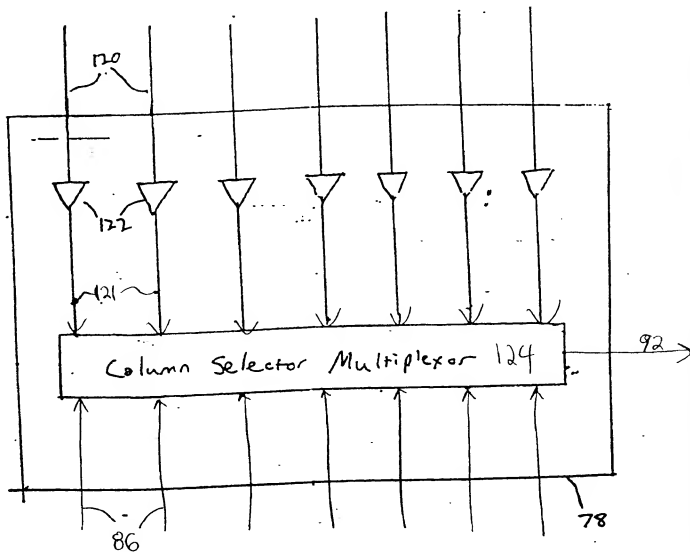


FIG. 2B

10001438.102401

FIG. 3A

Input from pixel array



1001438.102401

FIG. 3A

FOV-079

Input from pixel array

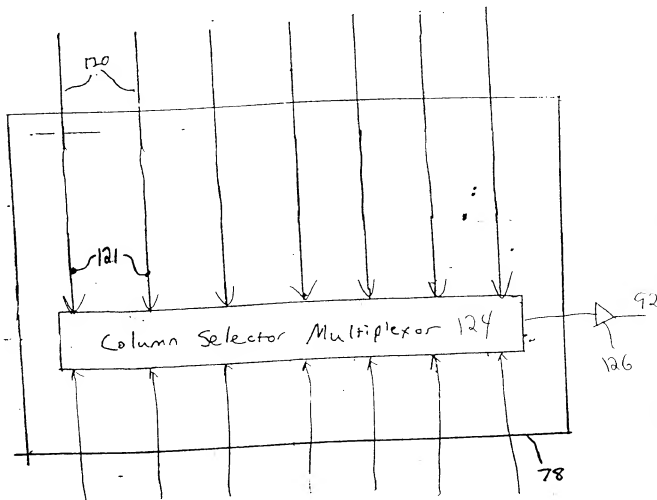
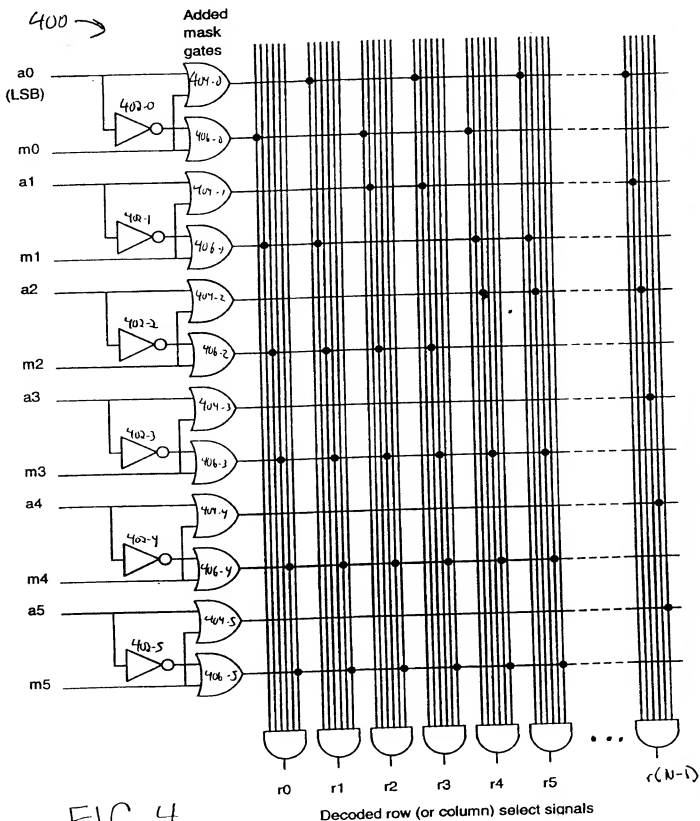


FIG. 3B

FOV-079



FOV-079

Agg-Bx-3

Agg-By-5

S12-0

S00



10001438-102401

Address
Bits

S02

Agg-Bx-2
Agg-Bx-4
Agg-Bx-8

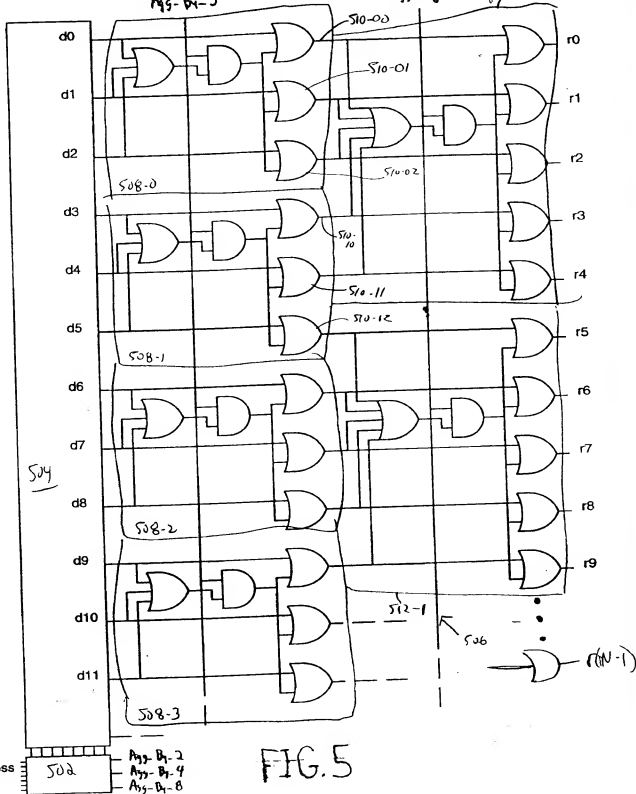
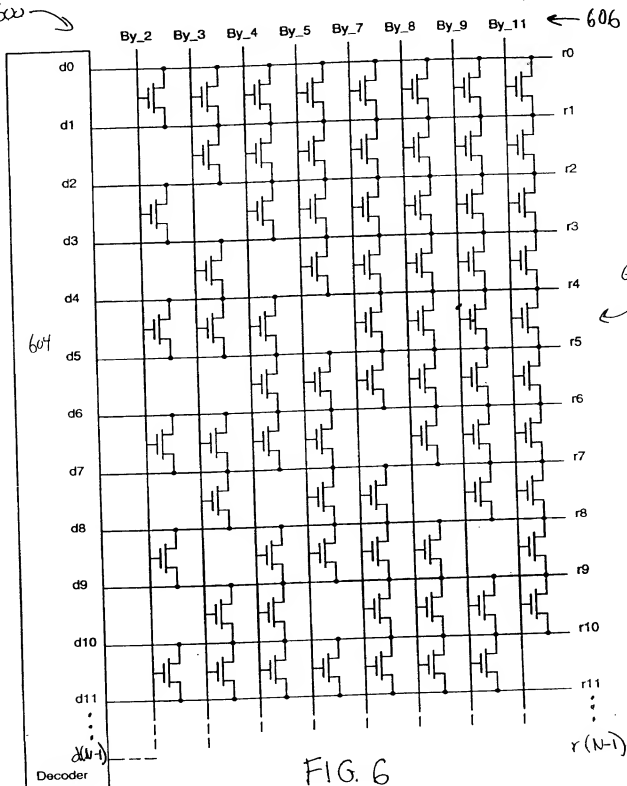


FIG. 5

10001438.102401

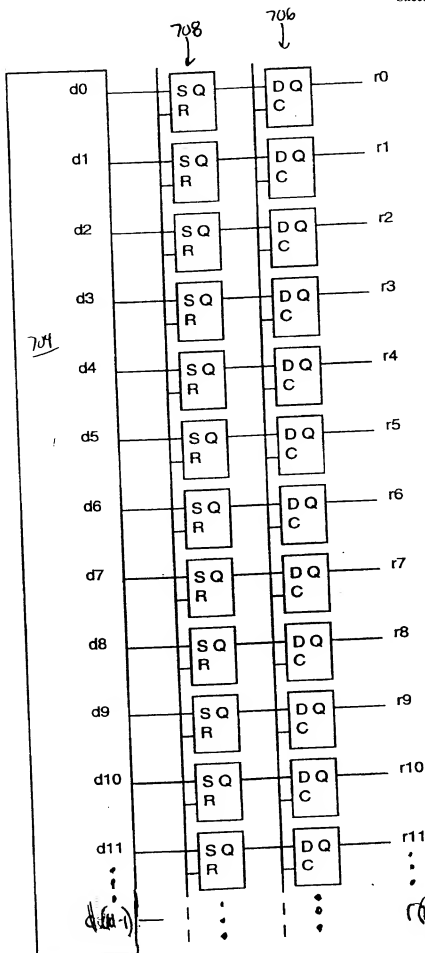
600

606



700

FIG. 7



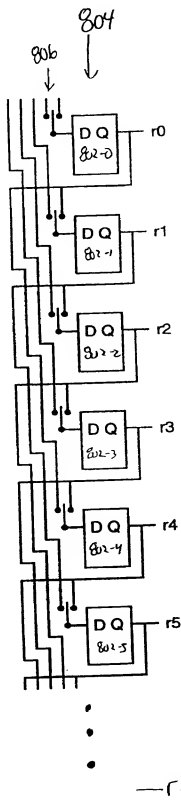


FIG. 8

104201-88410001

FIG. 9

1001438-102401

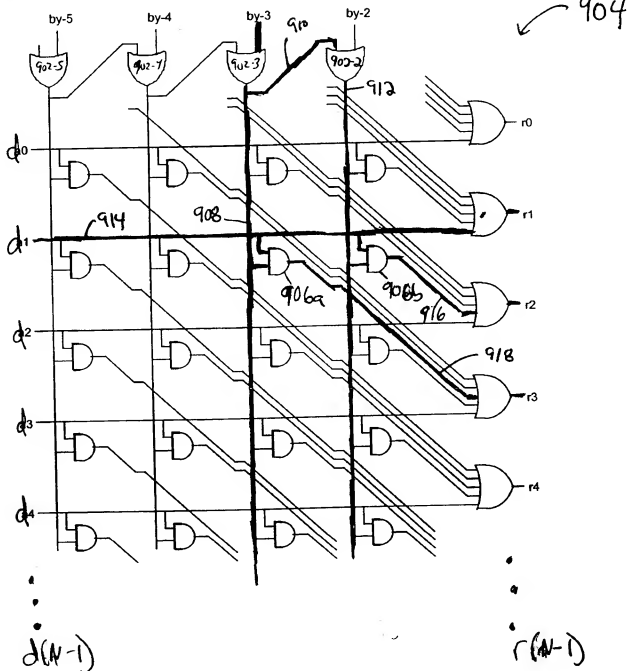


FIG. 9

900

	0		2			
0	Gr	R	Gr	R	Gr	R
	B	Gb	B	Gb	B	Gb
2	Gr	R	Gr	R	Gr	R
	B	Gb	B	Gb	B	Gb
	Gr	R	Gr	R	Gr	R
	B	Gb	B	Gb	B	Gb

FIG. 10A

	0	1		4	5	
0	Gr	R			Gr	R
1	B	Gb			B	Gb
4	Gr	R			Gr	R
5	B	Gb			B	Gb

FIG. 10B

10001438-102401